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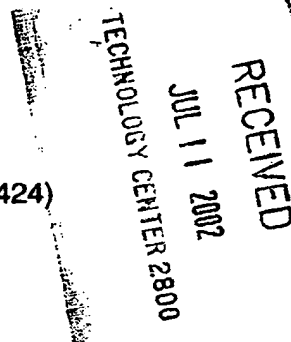
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Linder

Application Serial No. 09/875,501  
Filing Date June 4, 2001  
Inventor Klaus Florian Schuegraf  
Assignee Micron Technology, Inc.  
Group Art Unit 2815  
Examiner Edgardo Ortiz  
Attorney's Docket No. MI22-1741  
Title: Methods For Forming Wordlines, Transistor Gates, and Conductive Interconnects,  
and Wordline, Transistor Gate, and Conductive Interconnect Structures

**RESPONSE TO APRIL 8, 2002 FINAL OFFICE ACTION  
TO ACCOMPANY RCE FILING**

To: Box RCE  
Assistant Commissioner for Patents  
Washington, D.C. 20231

From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)  
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**AMENDMENTS**

**In the Claims**

Please replace the claims with the following clean version of the entire set of pending claims, in accordance with 37 C.F.R. § 1.121(c)(1)(i). Cancel all previous versions of any pending claim.

A marked up version showing amendments to any claims being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 C.F.R. § 1.121(c)(1)(ii). Any claim not accompanied by a marked up version has not been changed relative to the immediate prior